

(1) Remarks

Dear Examiner,

In the Final Office Action dated 09/01/2005, page 5 in remarks regarding our Affidavit of prior invention, an argument was made that there was insufficient evidence supporting diligence in reducing the invention to practice, between our last recorded diligence date of September 5, 2000, and the initial provisional filing date of October 9, 2002. In particular it was stated that further evidence is needed to establish diligence. In this response, we set out a clear recorded timeline of diligence between the dates of September 5, 2000 and October 9, 2002. Between those two dates many variations of the spot size converter were simulated, fabricated, and measured. We provide recorded highlights of those events.

With the evidence provided in the following continuation of the declaration of prior invention, we now maintain that the amended claims in our response to the First Office Action represent patentable novel structure over the cited prior art.

(3) Declaration filed under section 1.131

We request that the examiner take into account the following continuation of our affidavit of prior invention in our response to the First Office Action, pursuant to section 1.131.

Inventor's credentials

Dr. Little obtained a B.A.Sc. in Electrical Engineering in 1984, and a PhD in Electrical Engineering in 1994, both from the University of Waterloo, Canada. He held a Post Doctoral position, and subsequently a Research Associate position, at the Massachusetts Institute of Technology (MIT) for five years. He has held a Research Professor position and the University of Maryland, as well as positions at Nortel Networks and Fujitsu. He has consulted for a number fiber optic companies. In 2000, he founded Little Optics Inc, and served as its President and Chief Technology Officer. The company has since raised \$25M in venture capital. Little Optics Inc pioneered high index contrast photonic circuits. Dr. Little has published over 75 articles in peer reviewed journals and holds several patents in the field.

Place of Invention

The work performed in conceiving, analyzing, and reducing to practice of this invention was performed in the USA.

The following pages and Exhibits from my lab notebook and from a commercial photomask order, combined with those set forth in our response to the First Office Action, detail this invention as occurring before the priority dates of the cited prior art references (Appl. No. 10/006,752 to Lam et. al. and Appl. No. 10/083,674 to Zhou et. al.). They also set forth a timeline of diligence between the date of first documentation of invention and first filing of application, in reducing the invention to practice.

I declare that the data, devices and products of this application were conceived by me on the dates listed.



10/19/05

Brent Little

In our Response to First Office Action, we set forth our initial conception date of May 25, 2000. Between the initially recorded conception date of May 25, 2000, and our priority provisional application date of October 9, 2002 for this application, there was a continuity of diligent activity in analyzing and reducing the spot size converter to practice. The following recorded activities took place between those two dates:

(1) Exhibit A is a copy of page 35 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. The recording of these simulations is November 12, 2000.

(2) Exhibit B is a copy of page 37 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is December 9, 2000.

(3) Exhibit C is a copy of page 62 of Brent Little's "Little Optics Lab Notebook #3" showing more simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is August 15, 2001.

(4) Exhibit D is a copy of page 77 of Brent Little's "Little Optics Lab Notebook #3" showing more simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide imbedded in a larger waveguide. This mode transformer is similar to the one of this application, but has the smaller high confinement waveguide buried within the larger waveguide, rather than on top of. The recording of these simulations is October 6, 2001.

(5) Exhibit E is a copy of page 12 of Brent Little's "Little Optics Lab Notebook #4" showing sketches of photomask layout used to fabricate and quantify mode transformer performance. The recording of these simulations is February 24, 2002.

(6) Exhibit F is a copy of page 110 of Brent Little's "Little Optics Lab Notebook #3" showing simulations of a spot size mode transformer for input output (i/o) coupling to fibers incorporating a tapered waveguide. This mode transformer is similar to the one of this application. The recording of these simulations is March 2, 2002.

(7) Exhibit G is a copy of page 13 of Brent Little's "Little Optics Lab Notebook #4" showing simulations for a variation of the spot size mode transformer. This mode transformer is similar to the one of this application. The recording of these simulations is April 2, 2002.

(8) Exhibit H is a copy of page 14 of Brent Little's "Little Optics Lab Notebook #4" showing measured data of a fabricated device for the coupling efficiency of coupling from a fiber into a fabricated large waveguide portion of the mode transformer. The recording of these measurements is April 11, 2002.

(9) Exhibit I is a copy of page 20 of Brent Little's "Little Optics Lab Notebook #4" showing scanning electron microscope (SEM) images of a cross section of a fabricated variation of the spot size mode transformer having the small waveguide imbedded inside the large waveguide. The observed "seams" in the fabrication indicated that the small waveguide should be put adjacent to (below or on top) of the large waveguide as originally proposed in the lab book notes of May 25, 2000, August 22, 2000, and September 5, 2000. The recording of these measurements is April 25, 2002.

(10) Exhibit J is a copy of page 28 of Brent Little's "Little Optics Lab Notebook #4" showing simulations of a variation of the present mode transformer having more than two sections. The recording of these simulations is June 1, 2002.

(11) Exhibit K is a copy of a purchase order sent to DuPont Photomasks for the spot size converter of Exhibit F. The purchase order is for a set of photomask reticles that was subsequently used to fabricate the structure of Exhibit F.

Conditional Request for Constructive Assistance

Applicant has amended the specification and claims of this application so that they are proper, definite and define novel structure which is also unobvious. If, for any reason this application is not believed to be in full condition for allowance, applicant respectfully requests the constructive assistance and suggestions of the Examiner pursuant to M.P.E.P. section 2173.02 and section 707.07(j) in order that the undersigned can place this application in allowable condition as soon as possible and without the need for further proceedings.

Very Respectively,

A handwritten signature in black ink, appearing to read "Brent Little", with a stylized flourish at the end.

Brent E. Little

Applicant Pro Se



Exhibit A

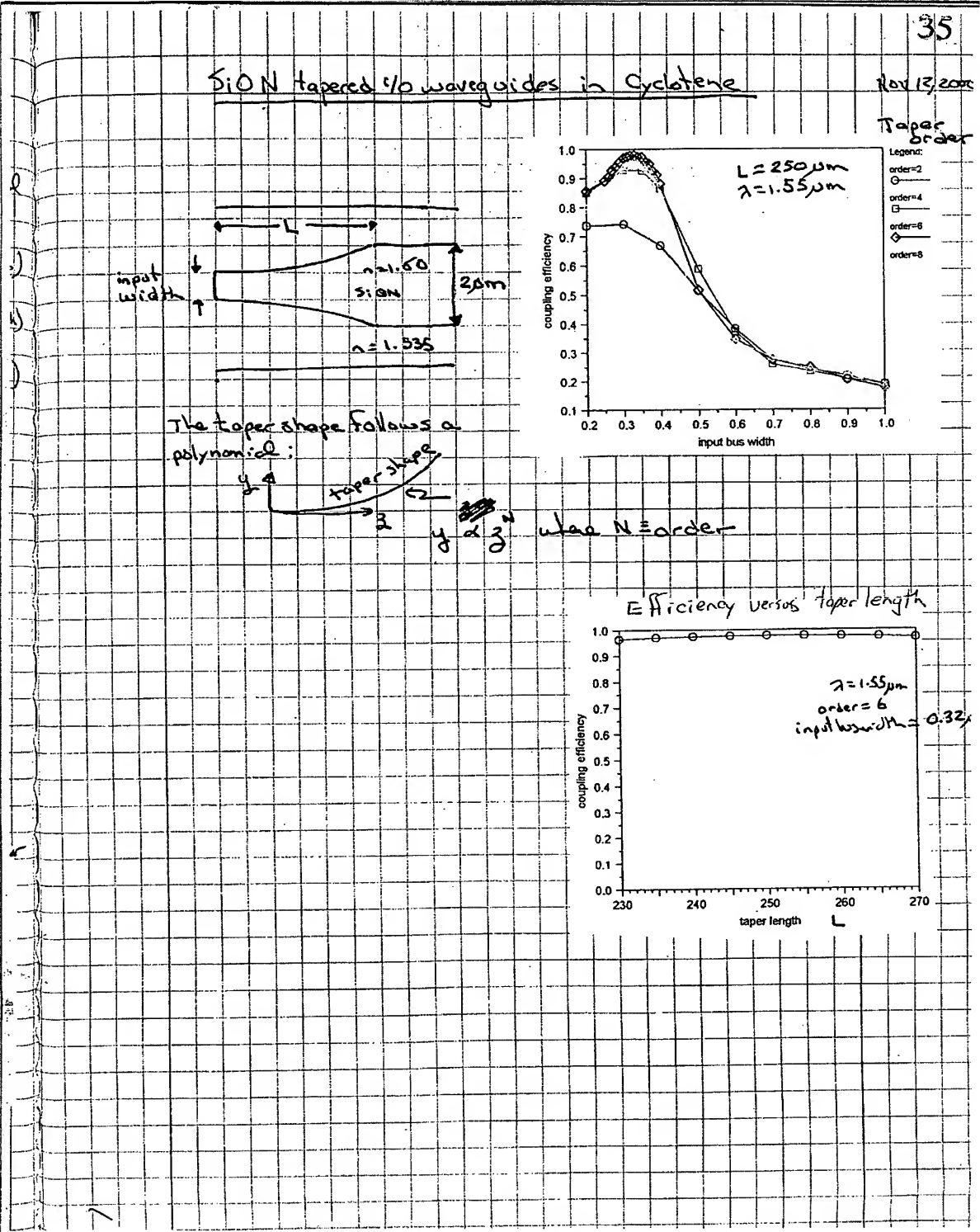
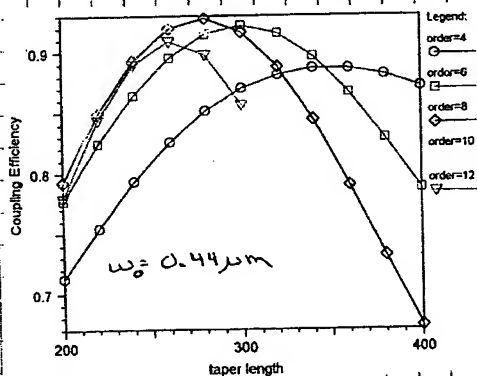
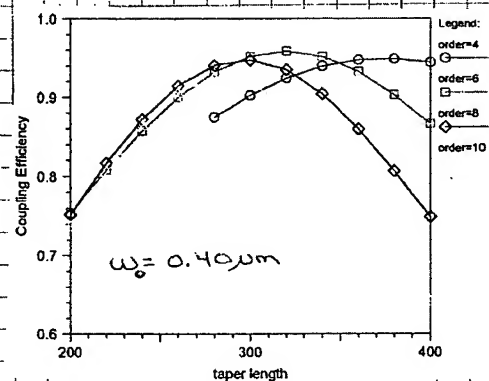
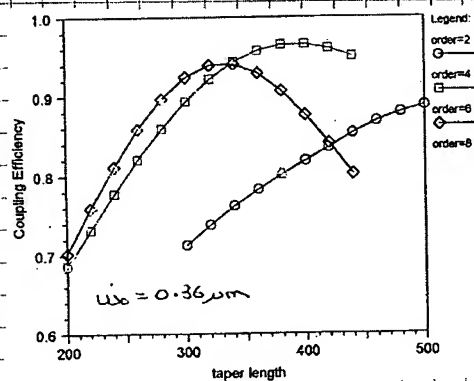


Exhibit B

37

SiON tapered I/O waveguides in BCB + FBCB

Dec 9, 2000



low index contrast waveguide for fiber matching

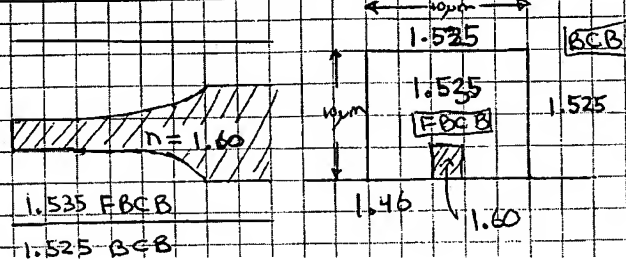


Exhibit C

62

More Mode transformer Simulations

Aug 15, 2001

Cross-Section

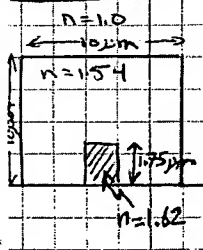
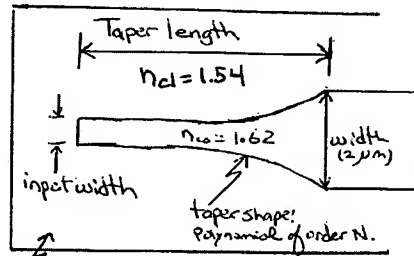
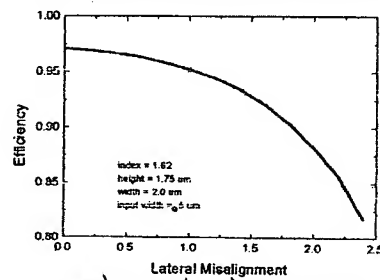
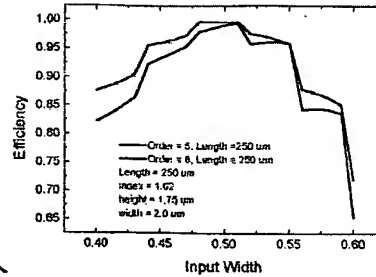
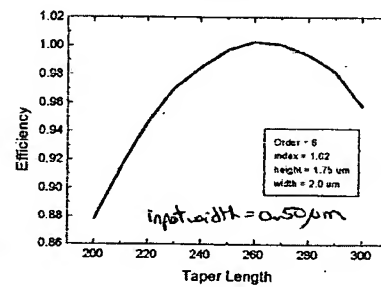
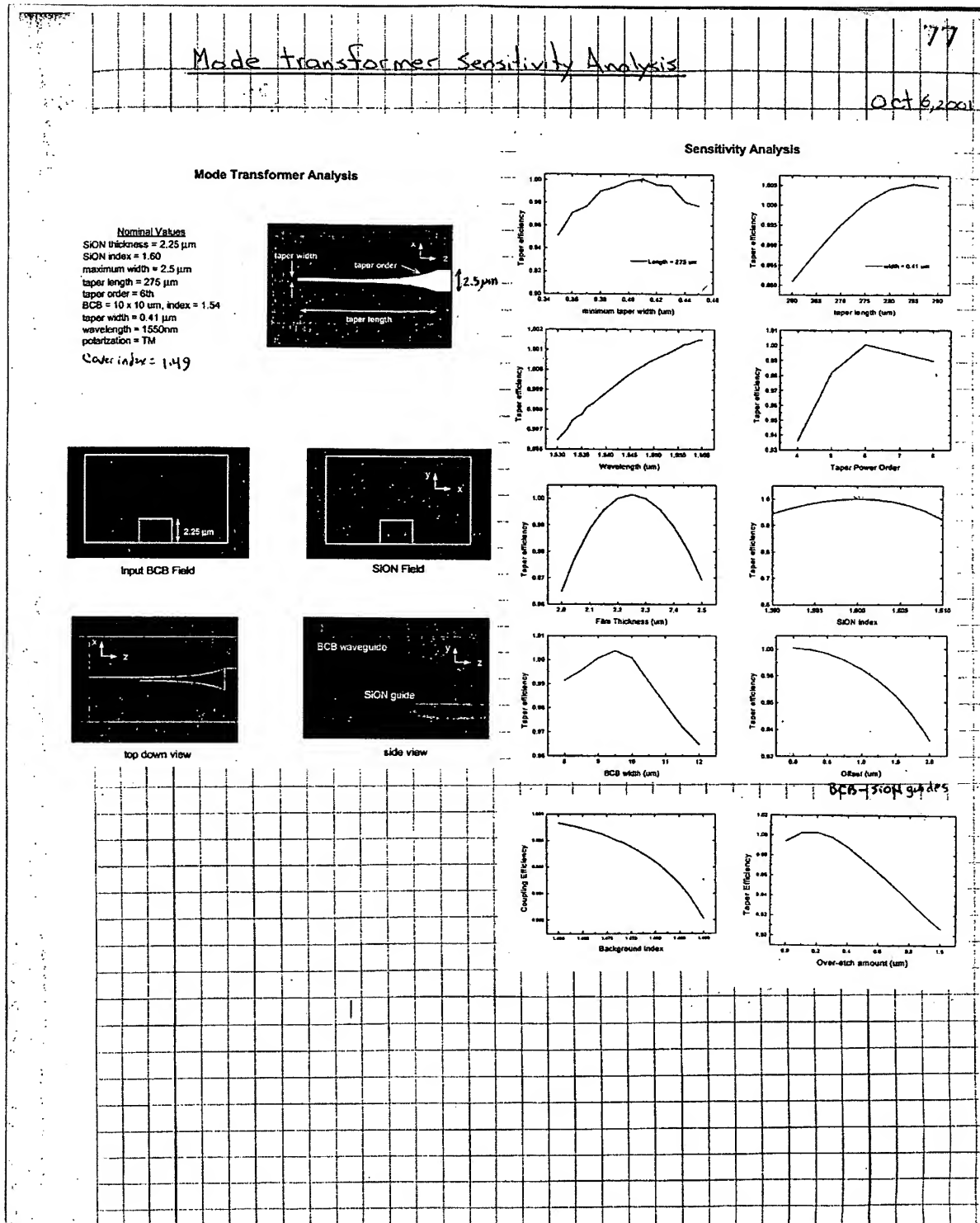
Top-Down $n=1.0$ fiber matched guide $10\mu\text{m} \times 10\mu\text{m}$ misalignment between sion
and the centre of the
BCS guide

Exhibit D



BEST AVAILABLE COPY

Exhibit E

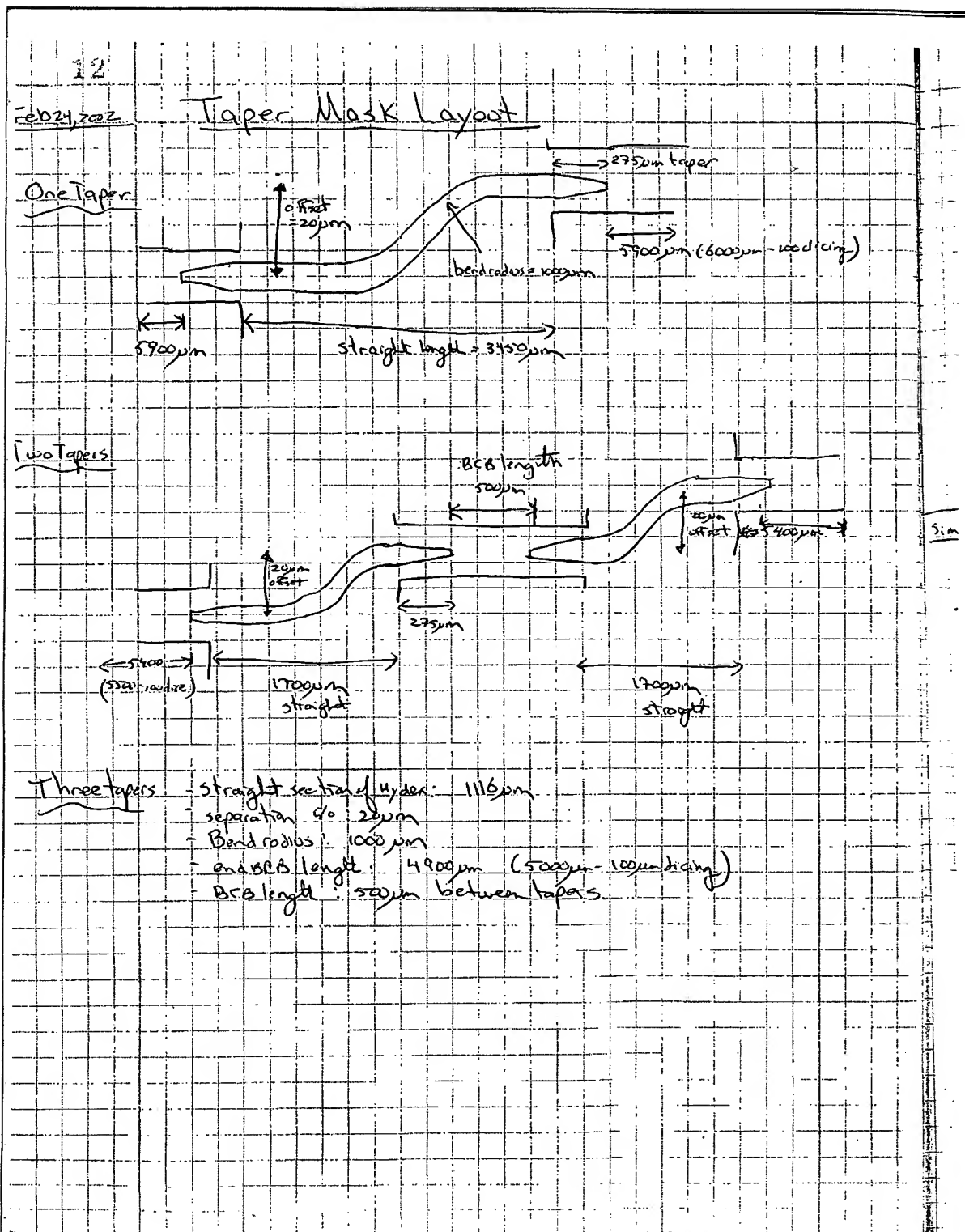
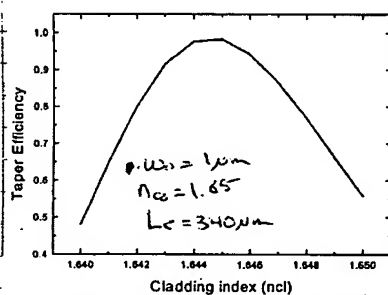
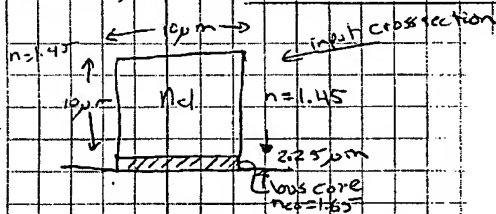


Exhibit F

110

March 2, 2002

March 3, 2002 Mode Transformer-based on Pinned-off waveguide

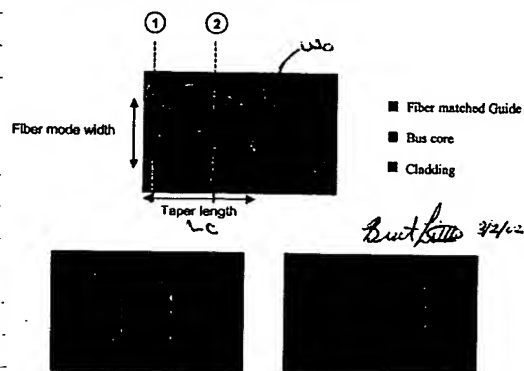


Simulation of Mode Transformer Efficiency:

Mode Transformers

Pinched off fiber guide

In this mode transformer, the bus core is situated below the large fiber matched waveguide, rather than imbedded in the larger guide. The width of the bus waveguide is made large, on the order of 10 μm , or as large as the fiber matched waveguide. The fiber matched waveguide is then tapered down to a point, typically with a linear taper.

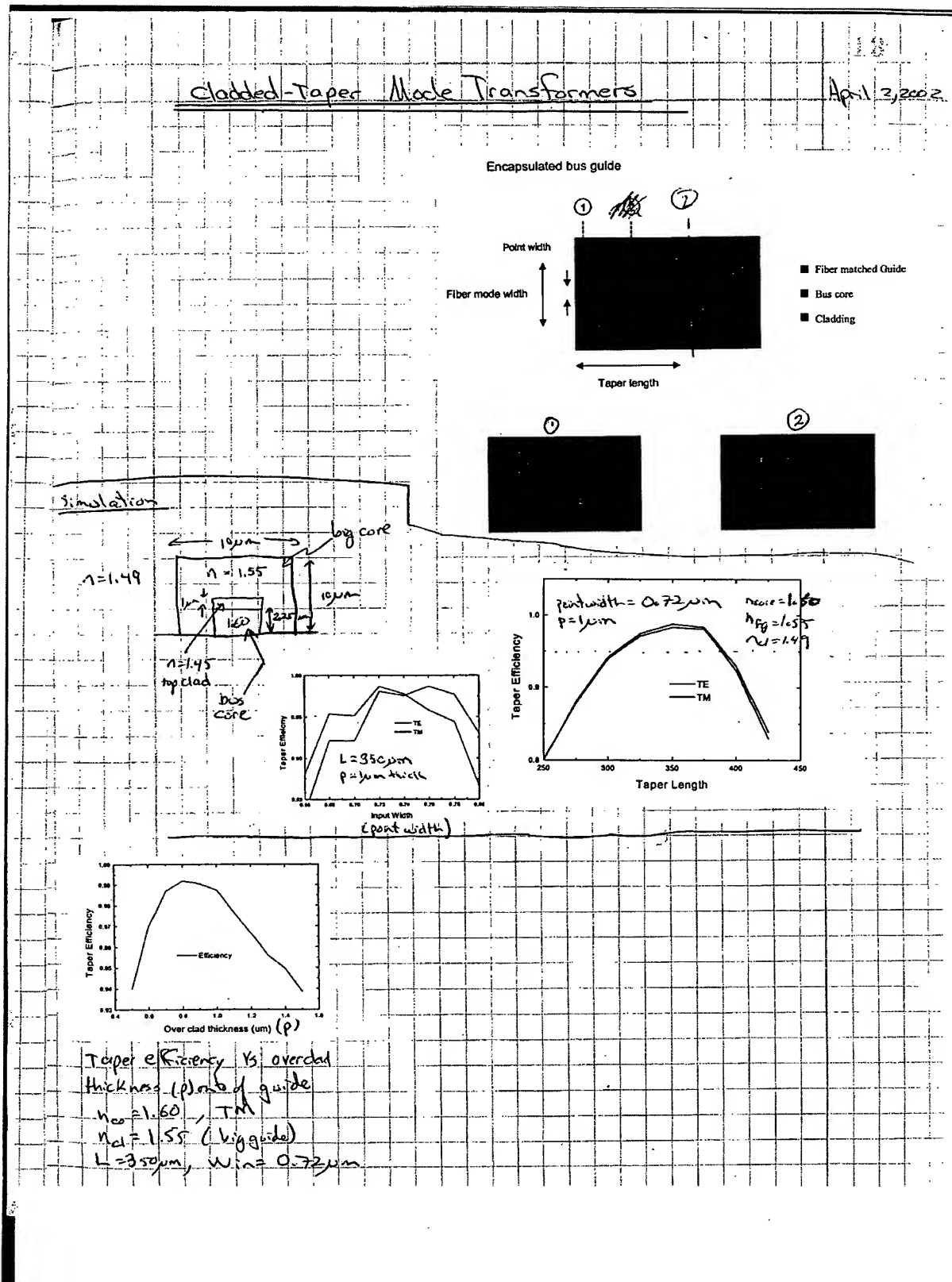


Burt Little 3/2/02

Bent Luth 3/2/02

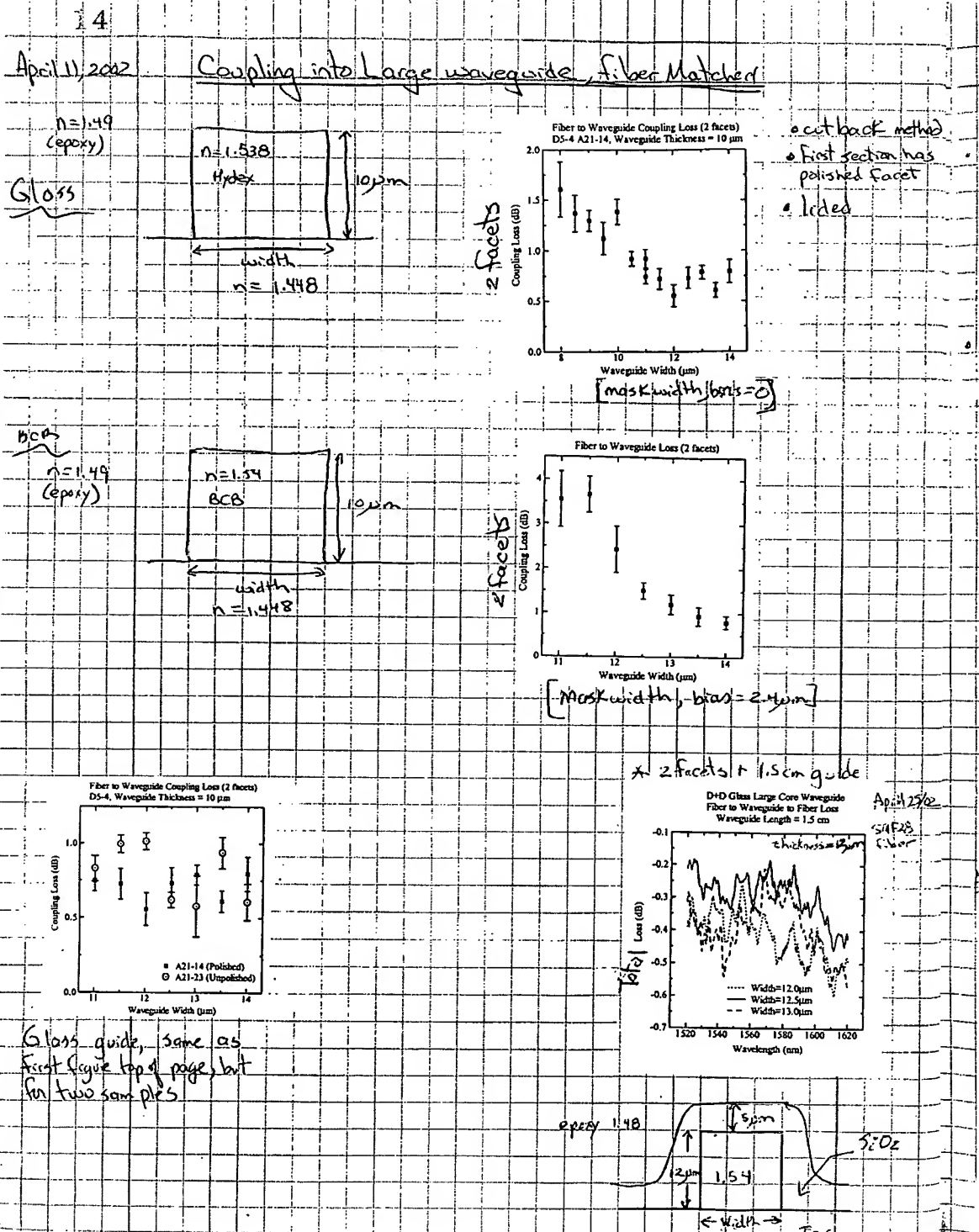
BEST AVAILABLE COPY

Exhibit G



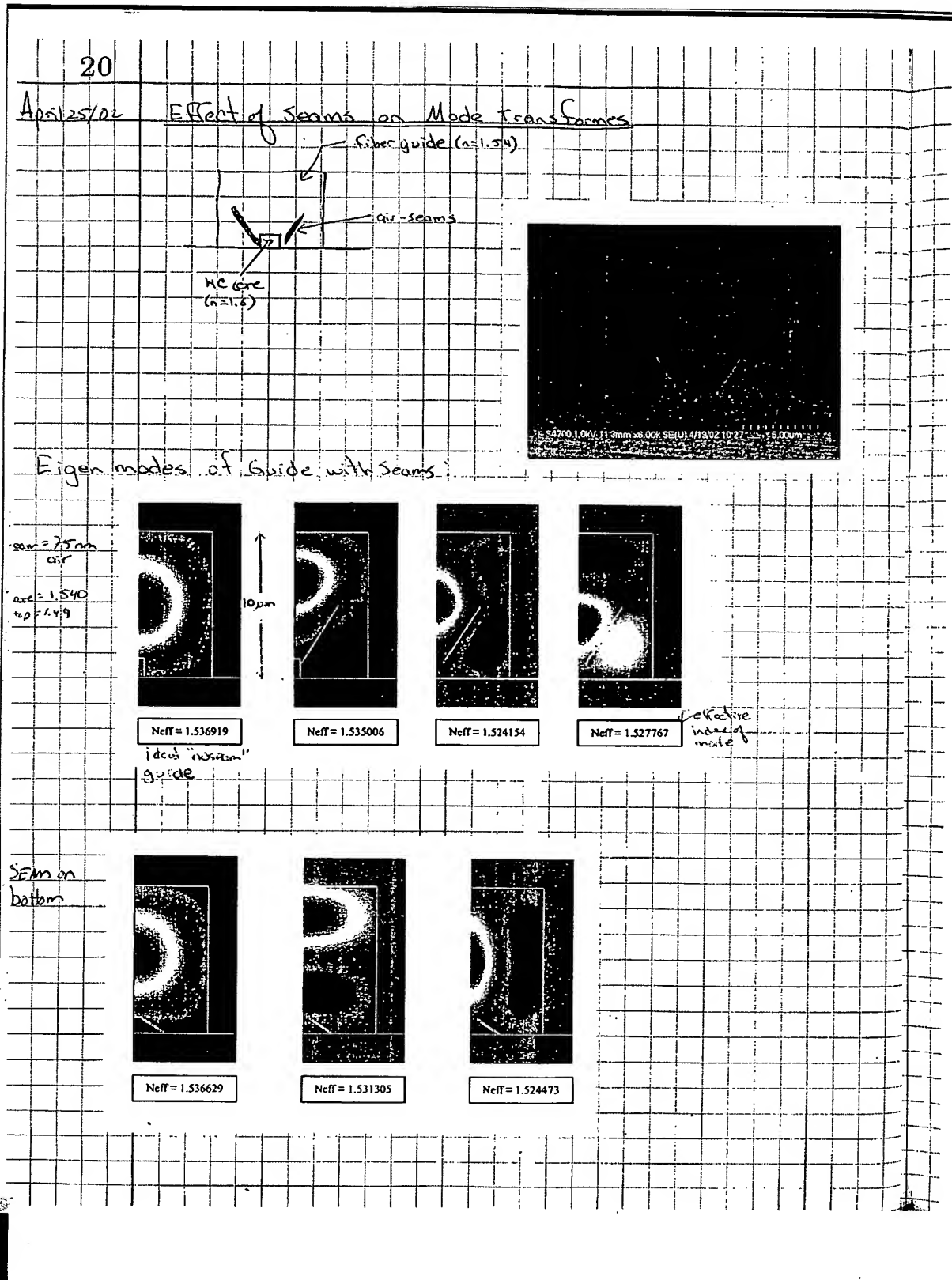
BEST AVAILABLE COPY

Exhibit H



BEST AVAILABLE COPY

Exhibit I



BEST AVAILABLE COPY

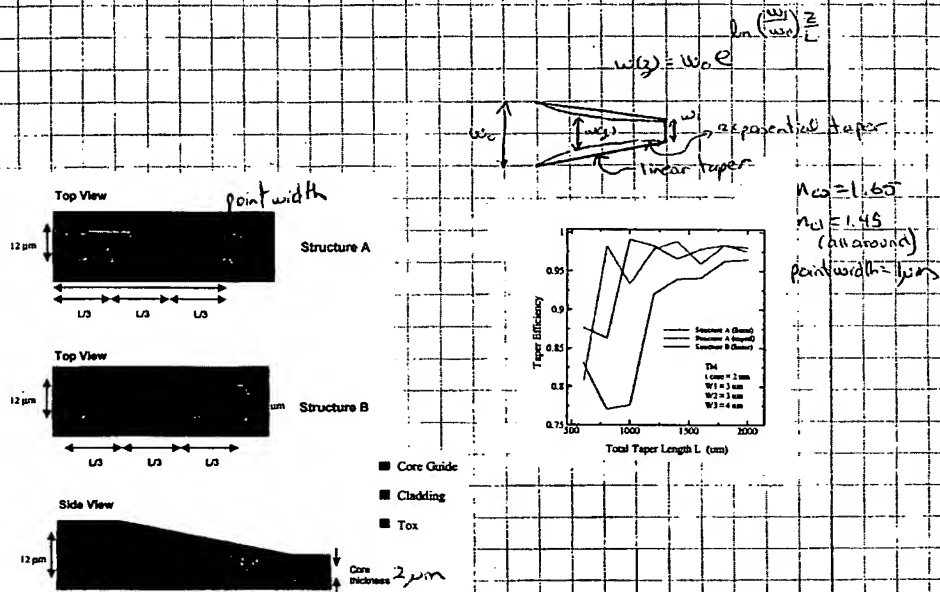
Exhibit J

28

June 1, 02

Staircase Taper

This taper approximates a vertical taper by means of discrete vertical steps (stair steps).



Note that the core thickness is 3 μm here, and the Taper Efficiency is calculated just after the coupling length L.

Brant Little
June 1, 2002

Exhibit K

Little Optics Inc.
9020 Junction Drive
Annapolis Junction, MD 20701

Purchase Order

Account #	Date	P.O. Number
	7/25/2002	01-478

Vendor:
DuPont Photomask, Inc.
P.O. Box 96089
Chicago, IL 60693

Ship to:
Little Optics, Inc.
9020 Junction Drive, Suite D
Annapolis Junction, MD 20701
Tel: (301) 604-7668
Attn: John Hryniewicz (301) 604-7668 x224

Item	Description	Qty.	Rate	Amount
TPR6 BUSWVG	Material: 6" x 6" .250" AR Q17 2 µm Data format: GDSII Address: 0.05 µm Write tool: Core 2564 Minimum feature: 1.25 µm Critical dimensions: 1.25 µm or larger CD tolerance: +/- 0.1 µm Registration: N/A Defects size: 1.5 µm Defect density: 0 defects / sq in Pellicle: none	1	2,845.00	2,845.00
TPR6 LARGE-12	Material: 6" x 6" .250" AR Q17 2 µm Data format: GDSII Address: 0.05 µm Write tool: Core 2564 Minimum feature: 1.25 µm Critical dimensions: 1.25 µm or larger CD tolerance: +/- 0.1 µm Registration: N/A Defects size: 1.5 µm Defect density: 0 defects / sq in Pellicle: none	1	2,845.00	2,845.00
Refer to Quote No. L0053002Q			Total	

Page 1

Little Optics Inc.
9020 Junction Drive
Annapolis Junction, MD 20701

Purchase Order

Account #	Date	P.O. Number
	7/25/2002	01-478

Vendor:
DuPont Photomask, Inc.
P.O. Box 96089
Chicago, IL 60693

Ship to:
Little Optics, Inc.
9020 Junction Drive, Suite D
Annapolis Junction, MD 20701
Tel: (301) 604-7668
Attn: John Hryniewicz (301) 604-7668 x224

Item	Description	Qty.	Rate	Amount
TPR6 LARGE-13	Material: 6" x 6" .250" AR Q17 2 µm Data format: GDSII Address: 0.05 µm Write tool: Core 2564 Minimum feature: 1.25 µm Critical dimensions: 1.25 µm or larger CD tolerance: +/- 0.1 µm Registration: N/A Defects size: 1.5 µm Defect density: 0 defects / sq in Pellicle: none		2,845.00	2,845.00
Refer to Quote No. L0053002Q			Total	54,535.00

Page 2

BEST AVAILABLE COPY



DuPont Photomask, Inc.

131 Old Settlers Blvd, Round Rock, TX 78664 (Corporate)
1901 East Morgan Street, Kokomo, IN 46901
100 Texas Avenue, Round Rock, TX 78664
2920 Coronado Drive, Santa Clara, CA 95054

Customer Requirements Form

1. Company: Little Optics, Inc. 2. Contact: John Hryniewicz 3. Phone: (301) 604-7668 x224; (301) 706-9928 4. Fax: (301) 604-8286
5. Date: 07/25/2002 6. Purchase Order #: 01-478 7. Email/Address: jchub@littleoptics.com
8. Device Name: TPR6.SF 9. Product Type: 5X 10. Stripper Type: (X) Ultrasonic-XLS () GCA () ASML
11. Substrate Type: (X) Quartz () Low Expansion-N/A () Soda Lime/White Crown
13. Substrate Thickness "mil": (X) 250 () 150 () 120 () 100 () 60
15. Write System: () Ebeam (X) Laser () Any CORE 2564
16. Plot: () 8.5"x11" () 36" Fracture result (GDSII) to ftp for approval (see letter)
17. Data Format Supplied: () AutoCad/DXF () CIF (X) GDS () Mebes () Other
18. CD Location: (X) Diagrams Attached () No CD
19. Supplied Data: (X) Right Reading () Wrong Reading 20. Nikon Mark Separation: X Y (X) No Marks
21. Substrate Size: () 4" () 5" (X) 6" () 7" () 7.25"
14. Substrate Reflectivity: (X) Anti-Reflective () High Reflective () Medium Reflective () See Through
Special Order Material

Critical Dimensions: Inspection Required: Yes (X) Guaranteed () No () Ship Data w/Plate (X) CD Tolerance: (X) Tolerance () Mean to Nominal CD Uniformity: () Range () 3 sigma () Dev. from Mean NIST Standard: (X) Yes () No	Registration Inspection: Inspection Required: Yes () Guaranteed () No (X) Ship Data w/Plate () Measure File Provided: () Yes (X) No Reference: () Layer () Grid () Array Runout () Die Fit () Insert Fit () Field Fit (IXUT) () Orthogonality	Defect Inspection: Inspection Required: Yes (X) Guaranteed () Visual Inspection (>3.0um) () No () Ship Data w/Plate (X) Klairs Required: () Yes () No () Prime () Test () Other Specific Layer(s): Defect Size: Clear 1.5 micron Dark 1.5 micron Defect Density: (X) 0 () 0.5 () 1.0 () 2.0 Defect Description: (X) Defects per sq. Inch () Defective Die per sq. Inch
--	---	--

DuPont Photomask, Inc.

Page 1

9/22/99djs